FEB 1 7 2004

Substitute for Form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Page 1 of 1

Application No.	10/734,464		
Filed	12/12/2003		
First Inventor	Nataraj, B.		
Art Unit			
Examiner			
Atty. Docket No.	NLMI.P146		

U.S. Patent Documents							
Examiner Initials*	US Patent Document		Name of Patentee or Applicant of Cited	Date of	Relevant Pages,		
	Number	Kind	Document	Publication	Columns, Lines		
DL	2002/0093347	A1	Henderson et al.	7/18/2002			
	6,195,277		Sywyk et al.	2/27/2001	/		
<u>. ]                                     </u>	6,199,140		Srinivasan et al.	3/6/2001	/-		
	6,230,236		Schultz et al.	5/8/2001	/		
	6,351,143		Guccione et al.	2/26/2002	/		
jι	6,661,686		Srinivasan	12/9/2003	1		

Non Patent Literature Documents					
Examiner Initials		Trans-			
pi	"Self-Timed Hit Circuit for a Content Addressable Memory," IBM Technical Dislosure Bulletin, Vol. 38, No. 2, Feb. 1995, pp. 65-66 plus cover and end page	/			

Examiner Signature Date Considered 4/05

<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.